

Master of Technology in VLSI Design

School of Electronics Engineering

CREDIT INFORMATION		
Sr. No.	Credit Summary	Credits
1	University Core	36
2	Programme Core	24
3	Programme Electives	16
4	University Electives	4
Total Credits		80

UNIVERSITY CORE [26]						
Sr. No.	Course Code	Course Title	Course type	T	P	C
1	MIT6001	Master's Project/Internship - 1				4
2	MIT6002	Master's Project/Internship - 2				24
Minimum of 3 Credits from courses in the Humanities basket						
1	ENG5001	Technical and Research Writing		1	2	2
All are Compulsory from Soft Skill Basket						
1	STSxxxx	Qualitative and Quantitative Skills Practice - 1	Lab only	3	0	3
2	STSxxxx	Qualitative and Quantitative Skills Practice - 2	Lab only	3	0	3

PROGRAMME CORE [24]						
Sr. No.	Course Code	Course Title	Course type	T	P	C
1	ECE5001	Digital System architecture and Design using HDL	Theory and Lab	3	2	4
2	ECE5002	Analog VLSI Design	Theory and Lab	3	2	4
3	ECE5003	CMOS Digital IC Design	Theory and Lab	3	2	4
4	ECE6009	VLSI Physical Design and Verification	Theory and Lab	3	2	4
5	ECE6002	VLSI Verification Methodologies	Theory and Lab	3	2	4
6	ECE6005	System-on-Chip Architecture Design	Theory and Lab	3	2	4

PROGRAMME ELECTIVES [16] Minimum of 16 Credits from courses in the Embedded System basket						
Sr. No.	Course Code	Course Title	Course type	T	P	C
1	ECE5004	Embedded System Architecture and Programming	Theory and Lab	3	2	4
2	ECE6007	Modelling of MOS Devices	Theory and Lab	3	2	4
3	ECE6004	Testing of VLSI Circuits	Theory and Lab	3	2	4
4	ECE6003	Low Power VLSI Circuits and Systems	Theory and Lab	3	2	4
5	ECE6001	Mixed Signal VLSI Design	Theory and Lab	3	2	4
6	ECE6008	Scripting Languages for VLSI	Theory and Lab	3	2	4
7	ECE6006	Digital Synthesis and Static Timing Analysis	Theory and Lab	3	2	4

UNIVERSITY ELECTIVE [04]						
Sr. No.	Course Code	Course Title	Course type	T	P	C
1	CSE5003	Data Structures	Theory and Lab	3	2	4